L	Hits	Search Text	DB	Time stamp
Number	nics	Search lext	55	_
1	2	5513369.pn.	USPAT;	2003/11/10
			US-PGPUB;	09:43
			EPO; JPO; DERWENT;	
			IBM TDB	
2	2	5708838.pn.	USPAT;	2003/11/10
1			US-PGPUB;	09:44
			EPO; JPO; DERWENT;	
			IBM TDB	
3	2	6567837.pn.	USPAT;	2003/11/10
			US-PGPUB;	09:44
ļ			EPO; JPO;	
			DERWENT; IBM TDB	
_	40	712/\$.ccls. and (distributed adj	USPAT;	2003/11/06
1	•	processing adj2 system) and interface\$1	US-PGPUB;	16:15
	:		EPO; JPO;	
			DERWENT;	
_	7	(712/\$.ccls. and (distributed adj	USPAT;	2003/11/05
	ĺ	processing adj2 system) and interface\$1)	US-PGPUB;	15:08
		and object\$loriented	EPO; JPO;	·
			DERWENT;	
	10175	((host core main controll\$3) adj2	IBM_TDB   USPAT;	2003/11/06
-	101/3	processor) and ((micro (digital adj	US-PGPUB;	16:20
		signal) co\$1 slave\$1 class function\$3)	EPO; JPO;	
		adj2 processor\$1)	DERWENT;	
	201	///bash sana main santuall(2) adi2	IBM_TDB	2002/11/05
<del>-</del>	281	(((host core main controll\$3) adj2 processor) and ((micro (digital adj	USPAT; US-PGPUB;	2003/11/05 15:10
		signal) co\$1 slave\$1 class function\$3)	EPO; JPO;	
		adj2 processor\$1)) and object\$1oriented	DERWENT;	
	1.51	and (interface\$1 API\$1 module\$1)	IBM_TDB	2002/11/05
_	151	((((host core main controll\$3) adj2 processor) and ((micro (digital adj	USPAT; US-PGPUB;	2003/11/05 15:33
		signal) co\$1 slave\$1 class function\$3)	EPO; JPO;	10.00
		adj2 processor\$1)) and object\$1oriented	DERWENT;	
		and (interface\$1 API\$1 module\$1)) and	IBM_TDB	
	35	@ad<19990707   (((((host core main controll\$3) adj2	USPAT;	2003/11/05
	33	processor) and ((micro (digital adj	US-PGPUB;	15:12
		signal) co\$1 slave\$1 class function\$3)	EPO; JPO;	
•	•	adj2 processor\$1)) and object\$1oriented	DERWENT;	
1		and (interface\$1 API\$1 module\$1)) and   @ad<19990707) and 712/\$.ccls.	IBM_TDB	
_	805	712/\$.ccls. and ((host core main	USPAT;	2003/11/05
		controll\$3) adj2 processor) and ((micro	US-PGPUB;	15:46
		(digital adj signal) co\$1 slave\$1 class	EPO; JPO;	
		function\$3) adj2 processor\$1)	DERWENT;	
-	63	(712/\$.ccls. and ((host core main	IBM_TDB USPAT;	2003/11/05
		controll\$3) adj2 processor) and ((micro	US-PGPUB;	15:39
		(digital adj signal) co\$1 slave\$1 class	EPO; JPO;	
	ŀ	function\$3) adj2 processor\$1)) and ((00	DERWENT;	
1		<pre>(object\$1 oriented) class\$2 object\$1) near1 processor\$1)</pre>	IBM_TDB	]
_	46	((712/\$.ccls. and ((host core main	USPAT;	2003/11/05
		controll\$3) adj2 processor) and ((micro	US-PGPUB;	15:39
		(digital adj signal) co\$1 slave\$1 class	EPO; JPO;	
		function\$3) adj2 processor\$1)) and ((00	DERWENT;	
	ļ	(object\$1 oriented) class\$2 object\$1)   near1 processor\$1)) and @ad<19990707	IBM_TDB	
-	288	(712/\$.ccls. and ((host core main	USPAT;	2003/11/05
	-	control1\$3) adj2 processor) and ((micro	US-PGPUB;	15:40
-		(digital adj signal) co\$1 slave\$1 class	EPO; JPO;	
		<pre>function\$3) adj2 processor\$1)) and ((00   (object\$1 oriented)) near10 processor\$1)</pre>	DERWENT; IBM TDB	
L	L	, () coot of officed, incarro processory	1 221 172	L

228 (712/3.ccis. and (host core main control133) adj2 processors and (micro (digital ad sigmal) col slave\$1 class function\$3, adj2 processors and (micro (digital ad) sigmal) col slave\$1 class function\$3, adj2 processors and (micro (digital ad) sigmal) col slave\$1 class function\$3, adj2 processors and (micro (digital ad) sigmal) col slave\$1 class function\$3, adj2 processor\$1) and (control1\$3) adj2 processor\$1) and (control\$3) adj2 processor\$1) and					
Functions33 adj2 processors11) and ((00)   coptest10 riented) near10 processors11   IBM_TDB   2003/11/05	_	228	controll\$3) adj2 processor) and ((micro	US-PGPUB;	
and @adx19990707   15:44   15:45   1			function\$3) adj2 processor\$1)) and ((00	DERWENT;	
			and @ad<19990707	_	
function\$3] adj2 processor\$1) and ((Oo) (object\$1 oriented)) near5 processor\$1)   MT 7DB   USPAT;   Controll\$3] adj2 processor\$1]   MT 7DB   USPAT;   Controll\$3]   MT 7DB   USPAT;   Controll\$3]   MT 7DB   USPAT;   Controll\$3]   MT 7DB   USPAT;	-	204	controll\$3) adj2 processor) and ((micro	US-PGPUB;	
155			function\$3) adj2 processor\$1)) and ((00	DERWENT;	
(digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((00) (object\$1 oriented)) near10 processor\$1)   module\$1) near5 processor\$1)   module\$1) near5 processor\$1)   module\$1) near5 processor\$1)   module\$1] near5 processor\$1]   module\$1] near5   modul	-	155	(((712/\$.ccls. and ((host core main	USPĀT;	
(object\$1 oriented)   near10 processor\$1)   and @adx(19990707) and ((interface\$1 API\$1   ((i7)12/8.ccls. and ((host core main control183) adj2 processor\$1) and (moro (object\$1 oriented))   near5 processor\$1)   and @adx(19990707) and ((interface\$1 API\$1   module\$1)   near5 processor\$1)   and @adx(19990707)   and ((interface\$1 API\$1   module\$1)   near5 processor\$1)   and (moro (object\$1 oriented))   near5 processor\$1)   and (interface\$1 API\$1   (interface\$1 API\$1   module\$1)   near5 processor\$1)   and (interface\$1 API\$1   (interface\$1 API\$1   module\$1)   near5 processor\$1)   and (interface\$1 API\$1   near5 processor\$1)   and (interface\$1 API\$1   near5 processor\$1)   (interface\$1 API\$1   near5 processor\$1)   (interface\$1 API\$1   near5 processor\$1)   and (interface\$1 API\$1   module\$1)   near5 processor\$1)   and (interface\$1 API\$1   module\$1   near5 processor\$1   interface\$1   api\$1   interface\$1   api\$1   interface\$1   api\$1   interface\$1   api\$1   interface\$1   interface\$1   api\$1   interface\$1   i			(digital adj signal) co\$1 slave\$1 class	EPO; JPO;	15:44
module\$1   near5 processor\$1)			(object\$1 oriented)) near10 processor\$1))		
control1\$3   adj2 processor) and ( micro (digital adj sigmal) co\$1 slave\$1 class function\$3   adj2 processor\$1)   and (00 (object\$1 cirented)) near10 processor\$1)   add21990707   add( interface\$1 API\$1   module\$1) near5 processor\$1)   add( interface\$1 API\$1   module\$1) near5 processor\$1)   add( interface\$1 API\$1   module\$1)   add21990707   add21990		155	module\$1) near5 processor\$1)	HCDAM.	2002/11/05
function(3) adj2 processor(31) and ((00   objects) oriented)) near10 processor(31)   near0 and (add(19990707) and ((interface(3) API(3)   near0 and (900   objects) oriented)) near5 processor(31)   near5 function(3) adj2 processor(31)   near5 function(3) adj2 processor(31)   near5 function(3) adj2 processor(31)   near5 function(3) adj2 processor(31)   near5 processor(31)	-	155	controll\$3) adj2 processor) and ((micro	US-PGPUB;	
and @ad<19990707) and ((interface\$1 API\$1 module\$1) near5 processor\$1)) and @ad<19990707 ((712/\$.ccls. and ((host core main control1\$3) adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1)) and ((00 (cbject\$1 oriented)) near5 processor\$1)) and ((00 (cdjet\$1 adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((00 (object\$1 oriented)) near5 processor\$1)) and ((interface\$1 API\$1 module\$1) near5 processor\$1) and ((interface\$1 API\$1 module\$1) near5 processor\$1) and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3] adj2 processor\$1) and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3] adj2 processor\$1) and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3] adj2 processor\$1) and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3] adj2 processor\$1) and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3] adj2 processor\$1] and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3] adj2 processor\$1] and ((interface\$1 adj signal) co\$1 slave\$1 class function\$3] adj2 processor\$1] and (interface\$1 adj signal) co\$1 slave\$1 class function\$3] adj2 pro			function\$3) adj2 processor\$1)) and ((00	DERWENT;	
Bad419990707   (712/2, ccls. and ((host core main controll\$3) adj2 processors) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1)) and ((oo (object\$1 oriented)) near5 processor\$1)) and ((interface\$1 API\$1 module\$1) near5 processor\$1)   (7(712/5, ccls. and ((host core main controll\$3) adj2 processor\$1)) and ((oo (object\$1 oriented)) near5 processor\$1)   and ((interface\$1 API\$1 module\$1) near5 processor\$1)   and ((oo (object\$1 adj oriented)) near5 processor\$1)   and ((oo (object\$1 adj oriented)) near5 processor\$1)   and ((interface\$1 API\$1 module\$1) near5   DERWENT; (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1)   and ((oo (object\$1 adj oriented)) near5   DERWENT; (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1)   and ((oo (object\$1 adj oriented)) near5   DERWENT; (object\$1 criented))   and ((oo (object\$1 criented))   an			and @ad<19990707) and ((interface\$1 API\$1	1BH_1DB	
Control1\$3) adj2 processors) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((oo (object\$1 oriented)) near5 processor\$1) and ((interface\$1 API\$1 module\$1) near5 processor\$1) and ((interface\$1 API\$1 module\$1 module\$1 module\$1 near5 processor\$1) and ((interface\$1 API\$1 module\$1 near5 processor\$1) and ((interfa	_	134	@ad<19990707	IISPAT:	2003/11/05
function\$3] adj2 processor\$1) and ((OO (object\$1 oriented)) near5 processor\$1) and ((Interface\$1 API\$1 module\$1) near5 processor\$1) ((1/712/\$.ccls. and ((host core main controll\$3) adj2 processor\$1 and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((interface\$1 API\$1 module\$1) near5 processor\$1)) and ((interface\$1 API\$1 module\$1) near5 processor\$1) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((OO (object\$1 adj oriented)) near5 processor\$1)		134	controll\$3) adj2 processor) and ((micro	US-PGPUB;	
and ((interface\$1 API\$1 module\$1) near5 processor\$1) (((712/\$.ccls. and ((host core main controll\$3) adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class processor\$1)) and ((interface\$1 API\$1 module\$1) near5 processor\$1) and (dating adj adj2 processor\$1)) and ((interface\$1 API\$1 module\$1) near5 processor\$1) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class processor\$1)  - 8 (712/\$.ccls. and ((host core main controll\$3) adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) - 50 ((host core main controll\$3) adj2 processor\$1) and ((00 (object\$1 criented)) same processor\$1) - 50 ((host core main controll\$3) adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((00 (object\$1 oriented)) same processor\$1) - 50 ((host core main controll\$3) adj2 processor\$1) and ((00 (object\$1 oriented)) same processor\$1) - 2 5708838.pn.  - 2 5095522.pn.  - 2 6072944.pn.  - 2 6072944.pn.  - 2 6072944.pn.			function\$3) adj2 processor\$1)) and ((00	DERWENT;	
Controll\$3 adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$11) and ((oo (object\$1 oriented)) near5 processor\$11) and ((interface\$1 API\$1 module\$1) near5 processor\$11) and ((adital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((micro (digital adj signal) co\$1 slave\$1 class processor\$1) and ((oo (object\$1 adj oriented)) near5 processor\$1) and ((oo (object\$1 adj oriented)) near5 processor\$1) and ((oo (object\$1 adj oriented)) near5 processor\$1) and ((oo (object\$1 adj oriented)) same processor\$1) and ((oo (object\$1 oriented)) near10 processor\$1) and (oo (object\$1 oriented)) and (oo (object\$1 oriented)) are processor\$1) and (oo (object\$1 oriented) are			and ((interface\$1 API\$1 module\$1) near5	_	
function\$3] adj2 processor\$1)) and ((OO (object\$1 oriented)) near5 processor\$1)) and ((interface\$1 API\$1 module\$1) near5 processor\$1) and (eadc19990707 (712/\$.ccls. and ((host core main controll\$3) adj2 processor\$1) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((OO (object\$1 adj oriented)) near5 processor\$1) (712/\$.ccls. and ((host core main controll\$3) adj2 processor\$1) and ((OO (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((OO (object\$1 oriented)) same processor\$1) (Object\$1 oriented)) same processor\$1) ((host core main controll\$3) adj2 processor\$1) and ((OO (object\$1 oriented)) same processor\$1) ((host core main controll\$3) adj2 processor\$1) and ((OO (object\$1 oriented)) same processor\$1) US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;	-	115	controll\$3) adj2 processor) and ((micro	US-PGPUB;	1
- and ((interface\$1 API\$1 module\$1) near5 processor\$1)) and @ad<19990707 7 (712/\$.ccls. and ((host core main controll\$3) adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1)) and ((00 (object\$1 adj oriented)) near5 processor\$1) - 8 (712/\$.ccls. and ((host core main controll\$3) adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((00 (object\$1oriented)) same processor\$1) - ((host core main controll\$3) adj2 processor\$1) and ((00 (object\$1oriented)) same processor\$1) ((host core main controll\$3) adj2 processor\$1) and ((00 (object\$1oriented)) same processor\$1) (object\$1oriented)) near10 processor\$1) - 2 5708838.pn.  - 2 5095522.pn.  2 6072944.pn.  - 2 6072944.pn.  - 2 6072944.pn.			function\$3) adj2 processor\$1)) and ((00	DERWENT;	
7 (712/\$.ccls. and ((host core main controll\$3) adj2 processors) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((OO (object\$1 adj oriented)) near5 processor\$1)  8 (712/\$.ccls. and ((host core main controll\$3) adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((OO (object\$1oriented)) same processor\$1)  ((host core main controll\$3) adj2 processor\$1) and ((OO (object\$1oriented)) same processor\$1) ((host core main controll\$3) adj2 processor\$1) adj2 processor\$1 and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) adj2 processor\$1 and ((OO (object\$1oriented)) near10 processor\$1) ((object\$1oriented)) near10 processor\$1)  2 5708838.pn.  2 5095522.pn.  2 6072944.pn.  2 6072944.pn.  USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT; US-PGPUB; IS:55			and ((interface\$1 API\$1 module\$1) near5	IBM_TDB	
(digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((00 (object\$1 adj oriented)) near5 processor\$1)	_	7	(712/\$.ccls. and ((host core main		
(object\$1 adj oriented)) near5   IBM_TDB			(digital adj signal) co\$1 slave\$1 class	EPO; JPO;	13:45
- 8 (712/\$.ccls. and ((host core main controll\$3) adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((OO (object\$1oriented)) same processor\$1)			(object\$1 adj oriented)) near5		
(digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((00 (object\$loriented)) same processor\$1)	-	8	(712/\$.ccls. and ((host core main	· ·	
- (object\$loriented)) same processor\$1) ((host core main controll\$3) adj2 processor) and ((micro (digital adj signal) co\$1 slave\$1 class function\$3) adj2 processor\$1) and ((00 (object\$loriented)) near10 processor\$1)  - 2 5708838.pn.  - 2 5095522.pn.  2 6072944.pn.    IBM_TDB USPĀT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPĀT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; US-PGPUB; US-PGPUB; US-PGPUB; US-PGPUB			(digital adj signal) co\$1 slave\$1 class	EPO; JPO;	
signal   co\$1 slave\$1 class function\$3   EPO; JPO; adj2 processor\$1   and ((OO (object\$1oriented)) near10 processor\$1)   IBM_TDB	_	50	(object\$loriented)) same processor\$1)	IBM_TDB USPAT;	
- 2 forcestation (object force) near 10 processor 1)			signal) co\$1 slave\$1 class function\$3)	EPO; JPO;	15:54
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; USPAT; US-PGPUB; EPO; JPO; USPAT; US-PGPUB; EPO; JPO; USPAT; US-PGPUB; EPO; JPO;			(object\$1oriented)) near10 processor\$1)	IBM_TDB	0000 /15 /05
DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; IBM_TDB USPAT; US-PGPUB; IBM_TDB USPAT; US-PGPUB; US-PGPUB; EPO; JPO;	-	2	5708838.pn.	US-PGPUB;	
- 2 5095522.pn. USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; USPAT; US-PGPUB; EFO; JPO; USPAT; US-PGPUB; EFO; JPO; EPO; JPO;				DERWENT;	
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	_	2	5095522.pn.	USPĀT;	
- 2 6072944.pn.   IBM_TDB USPAT; 2003/11/05 US-PGPUB; EPO; JPO;   I5:57				EPO; JPO;	
US-PGPUB; 15:57 EPO; JPO;	_	2	6072944.pn.	IBM_TDB	2003/11/05
DERWENT;			-	EPO; JPO;	
IBM TDB				DERWENT; IBM TDB	·

_	3	6615279.pn.	USPAT; US-PGPUB;	2003/11/05 15:58
			EPO; JPO;	13.30
			DERWENT;	
			IBM TDB	
_	2	5963745.pn.	USPAT;	2003/11/05
			US-PGPUB;	15:58
	į		EPO; JPO;	
•			DERWENT;	
			IBM TDB	
-	2	6567837.pn.	USPAT;	2003/11/05
			US-PGPUB;	15:58
		·	EPO; JPO;	
			DERWENT;	
			IBM_TDB	0000/11/105
-	14	((host core main control1\$3) adj2	USPAT;	2003/11/06
		processor) and ((micro (digital adj	US-PGPUB;	14:09
		signal) co\$1 slave\$1 class function\$3)	EPO; JPO;	
		adj2 processor\$1) and (public near6	DERWENT;	
1		memory) and (private near6 memory) and (protected near5 memory)	IBM_TDB	
	125		HICDAT.	2003/11/06
-	135	712/\$.ccls. and ((public protected private) near6 memory same processor\$1)	USPAT; US-PGPUB;	14:10
			EPO; JPO;	17.10
			DERWENT;	
	1		IBM TDB	
_	114	(712/\$.ccls. and ((public protected	USPAT;	2003/11/06
		private) near6 memory same processor\$1))	US-PGPUB;	14:16
		and @ad<19990707	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	75	712/\$.ccls. and (private near6 memory	USPAT;	2003/11/06
		same processor\$1)	US-PGPUB;	14:26
		-	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	(712/\$.ccls. and (private near6 memory	USPAT;	2003/11/06
1	]	same processor\$1)) and (public near6	US-PGPUB;	14:11
		memory same processor\$1)	EPO; JPO; DERWENT;	
			IBM TDB	
_	11	(712/\$.ccls. and (private near6 memory	USPAT;	2003/11/06
		same processor\$1)) and (protected near6	US-PGPUB;	14:11
		memory same processor\$1)	EPO; JPO;	
	1		DERWENT;	
	1		IBM TDB	
-	67	(712/\$.ccls. and (private near6 memory	USPAT;	2003/11/06
		same processor\$1)) and @ad<19990707	US-PGPUB;	14:26
			EPO; JPO;	
		·	DERWENT;	
			IBM_TDB	
<del>-</del>	68	712/\$.ccls. and (protected near6 memory	USPAT;	2003/11/06
		same processor\$1)	US-PGPUB;	14:26
			EPO; JPO;	
			DERWENT;	
_		/712/¢ gglg and /nrotogted noon6 man	IBM_TDB USPAT;	2003/11/06
-	55	(712/\$.ccls. and (protected near6 memory same processor\$1)) and @ad<19990707	USPAT; US-PGPUB;	2003/11/06 14:27
	1	Same Processortii and Gad<19990/0/	EPO; JPO;	17.61
	1		DERWENT;	
			IBM TDB	
_	1	((712/\$.ccls. and (protected near6 memory	USPAT;	2003/11/06
	]	same processor\$1)) and @ad<19990707) and	US-PGPUB;	14:28
		object\$loriented	EPO; JPO;	
	1	-	DERWENT;	
			IBM_TDB	
-	12	712/\$.ccls. and (star adj2 (configuration	USPĀT;	2003/11/06
		topology connection))	US-PGPUB;	17:03
			EPO; JPO;	
			DERWENT;	
	L		IBM_TDB	

-	6	((host core main controll\$3) adj2	USPAT;	2003/11/06
		processor) and ((micro (digital adj	US-PGPUB;	16:25
		signal) co\$1 slave\$1 class function\$3)	EPO; JPO;	1
		adj2 processor\$1) and ((star adj2	DERWENT;	
		(configuration topology connection))	IBM_TDB	
		near10 processor\$1)	_	
_	58	((host core main controll\$3) adj2	USPAT;	2003/11/06
		processor) and ((micro (digital adj	US-PGPUB;	16:29
		signal) co\$1 slave\$1 class function\$3)	EPO; JPO;	ļ
		adj2 processor\$1) and (star adj2	DERWENT;	
		(configuration topology connection))	IBM TDB	1
-	30	(((host core main controll\$3) adj2	USPAT;	2003/11/06
	·	processor) and ((micro (digital adj	US-PGPUB;	16:29
		signal) co\$1 slave\$1 class function\$3)	EPO; JPO;	
		adj2 processor\$1) and (star adj2	DERWENT;	
		(configuration topology connection))) and	IBM TDB	
		@ad<19990707	_	
-	74		USPAT;	2003/11/06
		connection)) near5 processor\$1	US-PGPUB;	17:03
		•	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	57	((star adj2 (configuration topology	USPAT;	2003/11/06
		connection)) near5 processor\$1) and	US-PGPUB;	17:08
	!	@ad<19990707	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	3	(((star adj2 (configuration topology	USPAT;	2003/11/06
		connection)) near5 processor\$1) and	US-PGPUB;	17:08
		@ad<19990707) and 712/\$.ccls.	EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	49	712/\$.ccls. and (star same processor\$1)	USPAT;	2003/11/07
		•	US-PGPUB;	14:00
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	44	(712/\$.ccls. and (star same processor\$1)	USPĀT;	2003/11/07
		) and @ad<19990707	US-PGPUB;	14:00
	[		EPO; JPO;	
	'		DERWENT;	
			IBM_TDB	
-	2	"20020019887"	USPĀT;	2003/11/07
			US-PGPUB;	16:07
1			EPO; JPO;	[
			DERWENT;	
			IBM_TDB	
-	57	(host adj processor) and processors and	USPĀT;	2003/11/07
		(star near3 (configuration config	US-PGPUB;	16:44
		connection))	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	47	((host adj processor) and processors and	USPĀT;	2003/11/07
1		(star near3 (configuration config	US-PGPUB;	16:44
1		connection))) and @ad<19990707	EPO; JPO;	
		,	DERWENT;	
			IBM_TDB	<u>                                      </u>
				<u> </u>